

B.Tech. Computer Science Engineering / Instrumentation Engineering / Electronics and
Telecommunication Engineering (NEP Pattern) Semester-I
STESC103 - Basics of Electrical & Electronics Engineering

P. Pages : 2

Time : Three Hours

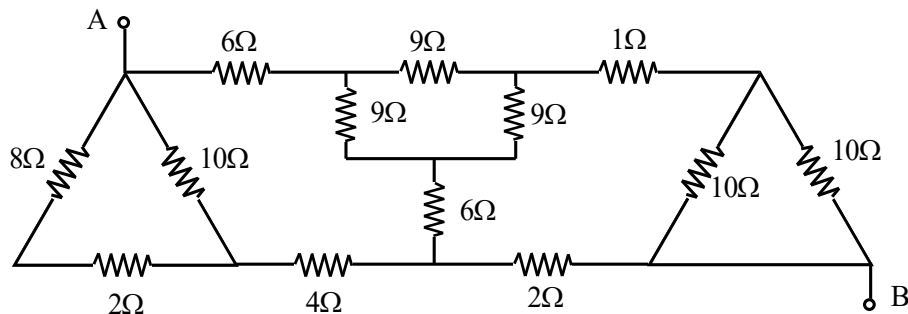


GUG/S/25/16154

Max. Marks : 80

- Notes :
1. All questions carry equal marks.
 2. Due credit will be given to neatness and adequate dimensions.
 3. Assume suitable data wherever necessary.
 4. Illustrate your answers wherever necessary with the help of neat sketches.
 5. Use of slide rule, Logarithmic tables, Steam tables, Mollier's chart, Drawing instruments, Thermodynamic tables for moist air, Psychrometric charts and Refrigeration charts is permitted.

1. a) Calculate the equivalent resistance across terminals A and B? Using star-Delta Transformation? 8

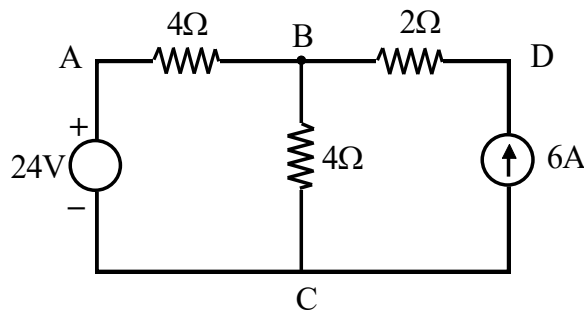


- b) State and explain KCL and KVL with one suitable example? 8

OR

2. a) Illustrate the types of sources used in electrical network. Write down the difference between ideal and practical sources used in the electrical network? Explain with a suitable diagram how we can convert current source into voltage source. 8

- b) Calculate the branch current using superposition theorem? 8



3. a) Define RMS and Average Value As applied to AC voltage. Also obtain their expression for sinusoidal voltage having the maximum value V_m . 8

- b) An alternating voltage is represented by $v = 243 \sin 369t$, find 8
- i) Maximum value
 - ii) Frequency
 - iii) Time period
 - iv) Instantaneous value of voltage when t is 5ms
 - v) Angular velocity

OR

4. a) Explain the series R-L-C circuit with suitable Phasor diagrams? 8
- b) A three-phase power system with a line voltage of 450V is supplying a delta connected load of 1200W at 0.8 pf lagging. Determine the phase and line current and also the phase impedance. 8
5. a) Explain the working Principle and Derive the EMF expression for Transformer? 8
- b) Draw and explain briefly with the help of neat sketch the circuit model of the transformer referred to primary side, explain every parameter involved in it. 8
- OR**
6. a) Draw the Phasor Diagram of Transformer for i) Leading, ii) lagging, iii) unity Power Factor Load? 6
- b) The emf per turn of a single phase 4400/440 V, 50 Hz transformer is 20V. calculate 6
- i) the number of primary and secondary winding turns,
- ii) the net cross-sectional area of core for a maximum flux density of 1.7 T.
- c) Why transformer cannot be operated using DC supply, explain briefly. 4
7. a) Explain briefly working of PN junction diode? Also explain application of diode as a half wave rectifier with neat circuit diagram and waveform? 8
- b) Derive the Gain of Non-inverting and inverting Op-amp? 8
- OR**
8. a) Differentiate the intrinsic and Extrinsic semiconductor? Explain the application of diode as full wave rectifier with circuit diag and waveform? 8
- b) Explain the characteristics of op-amp and concept of virtual ground? 8
9. a) Write down the truth table with its symbol for the following gates 8
- i) Ex-NOR ii) EX-OR
- iii) NAND iv) NOR
- b) Convert Binary to octal numbers for the following examples 4
- i) $(00101011)_2$ ii) $(101110011)_2$
- c) Convert this hexadecimal to equivalent decimal number? 4
- i) $(DAF1)_{16}$ ii) $(FA01)_{16}$
- OR**
10. a) Write down the simplified form of the Boolean expression. 8
- i) $Y = (\bar{A}.BC + D)(\bar{A}.D + \bar{B}.\bar{C})$ ii) $Y = A.B.C + \bar{A} + A.\bar{B}.C$
- b) Derive all basic gates by using universal Gates with truth table? 8
